Name:	Prasad S. Joshi Professor & Head Department of Electronics Eng.
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Date of Birth:	16th October 1970

Academic Qualification

Degree	Month/Year	College/University	Marks/Class
B.E.(Electronics)	May 1992	M. H. Saboo Siddik COE Mumbai University	62 % / First Class (Honors)
M. E. (Electronics)	May 2000	V. J. T. I. Mumbai University	69 % / First Class (Honors)
Ph.D. (Tech.) (Electronics Eng)	October 2016	V. J. T. I. Mumbai University	-

Experience

Period	Organization	Position	Job Profile
January 2017 – Till date	D. J. Sanghvi COE Vile Parle (west)	Professor & Head Dept. of Electronics.	Manage and Monitor dept. activities on a day-to-day basis. Plan for projects and help foster industry interaction.
August 2010 – December 2016	D. J. Sanghvi COE Vile Parle (west)	Associate Professor & Head Dept. of Electronics	
October 2009 – August 2010	D. J. Sanghvi COE	Head Dept. of Electronics	

	Vile Parle (west)		
August 2001 - October 2009	D. J. Sanghvi COE Vile Parle (west)	Assistant Professor (Dept of Electronics)	Teaching - TE, BE (µp/µc, Filter Theory, Advance DSP, Robotics) Handling departmental activities. Guiding Projects.
August 2001 - Sept. 1995	D. J. Sanghvi COE Vile Parle (west)	Lecturer (Dept of Electronics)	Teaching - FE, SE (BEE, C Prog. Electrical Net, PCOM) Guiding Projects.
Sept. 1995 - Sept. 1993	Shri Bhagubhai Mafatlal Polytechnic.	Lecturer (Dept. of IE)	Teaching - Diploma Students. Development of PCB lab.
Sept. 1993 - April 1993	X'pro Computers (Wipro – Franchisee)	Service Eng.	Servicing Desktops. Maintaining LAN at SHCIL

Project Handling & Invited Sessions

- Handling BE level Projects (in-house) since 1997.
- ME project: ECG Signal Analysis Using Wavelet Transforms. Tools: ECG signal simulator, Lab Windows CVI environment.
- Conducted CEP (Continuing Education Program) for college students in C, C++, VB and Java with fellow faculty in the period 1999 – 2001. Program had a successful run for three consecutive semesters, when the topics were introduced in the regular academic syllabus.
- Contributed concretely to the design development and implementation of 89C52 based 'Design Development Module'. Presently the same being used for conducting practicals in the subject of Microprocessors and Microcontrollers.
- Played a pivotal role in organizing an ISTE recognized Short Term Training Programme on 'Microcontrollers and Embedded Systems Design' conducted in the college between the period 27 November 2006 – 7 December 2006. Programme attended by 42 faculties from engineering colleges in Maharashtra.

- Designed and developed 'Temperature and Humidity Indicator' based on Atmel ATmega8L, for 'Saguna Baug', a farm located in Neral.
- Rendering consultancy based services to 'Deep Electronics', Vasai (an SSI involved in industrial product development), for developing a range of 'Analytical Lab Grade' equipments.
- Reasonable working and development experience in x51 and AVR core, as well as corresponding C cross compilers (Keil and GCC) and debug, development simulation environment.
- Chief contributor in the conduction of a 15 – day workshop for engineering college teachers held at Mukesh Patel School of Technology and Management Engineering, Juhu in May 2008.
- Delivered half a day session on 'Microcontrollers in Robotics' at Fr. Agnel College of Engineering Bandra in an ISTE sponsored Short Term Training Programme on 'Robotics'
- Executed the order for supplying custom designed 'Microcontroller Trainer Kits' to 'Mukesh Patel School of Technology and Management Engineering' in December 2008. This order worth Rs 50,000/- being the first to be awarded to D. J. Sanghvi COE's, consultancy development cell.
- Recipient of 'Best Teacher Award' for the academic year May 2008 – April 2009. Award constituted by SVKM management, includes a certificate of honor, and Rs 5,000/- in cash.
- Delivered a 3-hrs session, on 'Application of Microcontrollers in Robotics' with a demonstration session, at an ISTE sponsored STTP conducted at Fr. Conceicao Rodrigues COE, Bandra Mumbai, on 29th May, 2008.
- Convener of ISTE approved STTP on 'Effective Communication and Life Skills' conducted in the period 2nd June – 6th June 2008 at D. J. Sanghvi COE, VileParle (west).
- Delivered half-day session on PC based simulation for x51 using Keil, with practical sessions, at Fr. Conceicao Rodrigues COE, Bandra, Mumbai on 24th September 2008.
- Contributed for 2-days training in Keil – x51 cross compiler, and Orcad – PCB design and simulation, at ISTE recognized STTP conducted at Don Bosco Institute of Technology in November 2008.
- Delivered a 2-day session with fellow colleague on – Orcad as a PCB Design and Simulation tool, and Keil C-cross Compiler, at Don Bosco Institute of Technology, Kurla, Mumbai on 24th and 25th November 2008.
- Selected and Registered as a Doctoral candidate at Veermata Jijabai Technological Institute(VJTI), Matunga, Mumbai in December 2008.
- Delivered a 2-hrs session on 'Practical Aspects in Orcad as a Simulation Tool' at an ISTE approved STTP on 'Simulation Tools

and Languages' at K. C. College of Engineering, Thane on 15th January 2009.

- Delivered lecture and demonstration session with hands-on session on 'Keil – C cross Compiler' for the students of third year EXTC K. J. Somaiya COE, Sion, Mumbai on 20th and 21st February 2009.
- Delivered an expert session for faculties at Don Bosco Institute of Technology, Kurla, Mumbai on 'Microcontroller based Product Design' organized by department of EXTC, on 21st March 2009.
- Delivered a 3-hour session for third year students at Thakur College of Eng. Kandivli (west). Topic - 'Microcontrollers and Embedded Programming' on 31st August 2009.
- Appointed as I/C Head – Department of Electronics Engineering in October 2009, and later confirmed as Head – Department of Electronics.
- October 2009-Invited as a panel judge for Narottam Sakhsaria Foundation national level scholarship program in Engineering.
- Invited to deliver an expert session for second year students at Thakur College of Eng. Kandivli (west). Topic - 'Orcad- Layout and PCB Designing' on 18th February 2010.
- February 2011 – Invited for one day expert session at Fr. Agnel Vashi for conducting a demo/interaction session for students of Third Year on the topic 'Embedded Systems – Design, Debug and Testing Tools'
- October 2010-Invited as a panel judge for Narottam Sakhsaria Foundation national level scholarship program in Engineering.
- March 2011 - Delivered a One-day session at Fr. Agnel Bandra, at Short Term Training Program, in 'Electronic Product Design', on the topic 'PCB – Design and Layout considerations', with hands-on training to participants comprising of teacher and students.
- October 2011-Invited as a panel judge for Narottam Sakhsaria Foundation national level scholarship program in Engineering.
- November 2011 – Delivered a, 2-hrs, session (with demo) at Khalsa College Mumbai as a part orientation for the faculty members of Mumbai University BSc. - IT (Faculty), on the topic 'Embedded Systems - Architectures and Overview'.
- January 2012 – Participated in Poster Presentation at V. J. T. I., Matunga, Mumbai on the topic 'Radial Pulse Analysis', won 2nd prize.
- February 2012 – Delivered a 2-hrs. session at Sardar Patel Institute of Technology (SPIT) Mumbai, on the topic 'C for Embedded Development', for the students of TE(Electronics)
- 18th and 19th May 2012 – Adviser to Interview Board for 'Senior Radio Technician ' post at GoI's Staff Selection Commission (WR).

- 25th June – 4th July 2012 – Attended a 10-day workshop on 'Research Methodology' conducted by IIT- Bombay.
- 10th, 11th July 2012 – Invited as a guest speaker for Student Development Program, at Thakur College of Eng. Kandivli, on the topic 'Embedded Systems Development' , for the students of BE(Electronics, Extc., and Computers)
- October 2012 – Visiting NBA committee, audits the department and grants Acreditation for two years for the period April 2013 – March 2015.
- July 2013 – Delivered a 2-hrs. session on 'Embedded Systems and Processors', at Fr. C. Rodrigues Institute of Technology', Vashi, at AICTE sponsored National level STTP on 'VLSI and Embedded Systems'
- Recipient of 'Best paper award' and awarded the 1st prize at International Conference 'Update Ayurveda - 2014', organized by TNMC & BYL Nair Hospital, Mumbai, in December 2014..
- Awarded 1st prize for concept note presented at International Conclave on Health Tech. Innovations, organized by SAMEER, ICMR, NHSRC and DeitY at IIT – B, in January 2015.
- 13th, February 2015 – Delivered a 2-hrs. session entitled - 'Skill Enhancement Lecture', at Vivekananda Education Society's Institute of Technology, Department of Electronics Engineering, on the topic 'Microcontrollers and Embedded Systems' for the second year students.
- 7th March, 2015 – Invited to deliver a session at one day national workshop on "Recent Trends in Embedded Systems", organized by Department of Electronics and Telecommunications Engineering, Pillai HOC College of Engineering and Technology. Session topic 'C for Embedded Architectures'
- July 2015 – Invited to conduct a two days teachers training program cum workshop on 'AVR architecture' and 'Intel Galileo' platform by department of Electronics and Telecommunication Engineering, Thakur College of Engineering and Technology, Kandivali, Mumbai.
- 3rd October, 2015 – Invited for session on 'PC based hardware projects', at inter collegiate one day seminar for diploma students 'Pro – Search '15', organized by Department of Comp. Sci. Eng., SBMP, VileParle(w).
- 14th July – 16th July, 2016 and on 19th August 2016, successfully conducted workshop for students of Electronics and Biomedical Engineering (TE), on the open source PCB design and layout tool – KiCaD.
- 19th October 2016, successfully defended the thesis 'Radial Pulse Analysis', for Doctorate of Philosophy (Technology) in Electronics

Engineering, from Veermata Jiabai Technological Institute under Mumbai University.

- 22nd December 2016, selected and appointed as, Professor in department of electronics engineering.
- 14th January 2017, invited to deliver expert session on 'PC – Based Hardware Projects', at 'Pro-Search 2016', a workshop for final year students of Computers and IT, organized by Department of Comp. Sci. Eng., SBMP, Vile Parle (w).
- 16th February 2017, invited to deliver an expert session on 'Introduction to Embedded Development' for final year students of Diploma in Industrial and Digital Electronics at SBMP, Vile Parle (west).
- 24th February 2017, invited as judge for technical paper presentation competition at ICWET – 2017 organised by Thakur College of Engineering and Technology, Kandivali (east).
- March 2017 – Invited as convener, syllabus framing committee in the subjects of 'Miroprocessor and Peripherals' (at Sem 4th); 'Microcontrollers and Applications' (at Sem 5th); 'Embedded Systems Development' (at Sem 6th) Electronics Engineering for Mumbai University syllabus revision R-2016.
- 17th March 2017, invited by 'IEEE-CRCE Student branch', of Fr. Conceicao Rodrigues College of Engineering, Bandra, as a judge for state level intercollegiate project competition.
- 30th May 2017, invited by Thakur College of Engineering and Technology, Kandivali (east) as 'Subject Expert' for University Staff Selection Committee (USSC) for faculty position in department of Electronics Engineering.
- 5th July 2017 invited as a resource person in AICTE-ISTE approved STTP organized by Shah & Anchor Kutchhi Engineering College, Chembur, for conducting one-day workshop on 'Advanced Embedded and Real Time Application Design'.
- 26th July 2017 invited by department of Biomedical Eng – D. J. Sanghvi CoE, to conduct a one-day workshop on 'Introduction to Cortex M Architecture' with hands-on practice on the 'mbed' platform.
- 15th February 2018 invited by Narotam Sekhsaria Foundation for being a panelist for selection of award 'All round Excellence to the children of Police Personnel' studying in the final year of their Professional courses.
- 16th March 2018, invited by 'IEEE-CRCE Student branch', of Fr. Conceicao Rodrigues College of Engineering, Bandra, as a judge for state level intercollegiate project competition.
- 26th July 2018 invited by Department of Physics, Sathaye College, VileParle(west), for conducting a one day workshop on PCB Making

using open-source tool KiCAD for the students of T.Y. B.Sc (Physics)

- 20th December 2018, invited as panel member to review research paper presentation of students from various IIT's, in the track 'Mechatronics, Autonomous and Robotics System Design' at the forum 'IIT – Tech Meet', hosted by IIT-B.
- Invited to deliver expert session on topic 'Embedded Hardware for IoT', for final year students of IE & DE departments of Shri Bhagubhai Mafatlal Polytechnic, VileParle(west). Session conducted on 8th March 2019.
- Invited as member expert for Progarmwise Board of Studies (PBOS) of department of Electronics Engg., Shri Bhagubhai Mafatlal Polytechinc, VileParle (w). Attended meeting as PBOS member, 9th August 2019.
- Invited to deliver expert session on topic 'Recent Trends – Embedded Hardware for IoT', for final year students of Comps and IT departments of Shri Bhagubhai Mafatlal Polytechnic, VileParle(west). Session conducted on 1th October 2019
- Invited to deliver expert session, 'Endeavors for using technology to develop implements for providing solutions in health care and farming', at the AICTE – ISTE sponsored FDP on 'School of Design Thinking', at Rajiv Gandhi Institute of Technology, Andheri Mumbai, on 4th December 2019.
- 9th December 2019 invited by Narotam Sekhsaria Foundation for being a panelist and judge for selection of 'Excellence Award & Certificates of Merit' for students studying in the final year of their Professional courses.
- Session Chair at 6th International Conference on Advances in Computing at Fr. Conceicao Rodrigues College of Engineering, on 21st December 2019.
- Invited as member expert for Progarmwise Board of Studies (PBOS) of department of Electronics Engg., Shri Bhagubhai Mafatlal Polytechinc, VileParle (w). Attended meeting as PBOS member, 12th February 2020.

Project & Research Grants
- January 2017, Awarded a grant of ₹28,000/- under Mumbai University's, Minor Research Grant for project proposal, as pi, on 'Ayurveda, based objective analysis and interpretation of radial pulse signal for diagnosis and validation by controlled experiments', in collaboration with department of Clinical Pharmacology - TN Medical College & BYL Nair Hospital, with Dr. Renuka Munshi as co-investigator.
- March 2018, Awarded a grant of ₹35,000/- under Mumbai University's, Minor Research Grant for project proposal, on 'Farming implement for comprehensive temperature, humidity and soil organic carbon content estimation and logging', as co-investigator.
- March 2019, Awarded a grant of ₹ 30,000/- under Mumbai University's, Minor Research Grant for project proposal, on 'Exploring In-Situ Mechanisms For Estimating Soil Organic Carbon Content Towards Sustainable Farming', as co-investigator.
- March 2020, Awarded a grant of ₹ 30,000/- under Mumbai University's, Minor Research Grant for project proposal, on 'Assessment and evaluation of 'Saguna Rice Technique' (SRT) by evaluating and analyzing methane gas emission in light of traditional rice cultivation technique. (A need-based collaborative project in association with Shri. Chandrashekhar Bhadsavle, a practicing farmer at 'Saguna Baug', Neral, Maharashtra)', as principal investigator.

Publications
- Joshi P, Daruwala R (2013) Investigations into TIM perspective of Radial Pulse Analysis. Point-of-Care Healthcare Technologies (PHT), 2013 IEEE, p. 80-84 , Jan. 2013. doi : 10.1109/PHT.2013.6461289

- Joshi P, Kelkar P and Daruwala R (2013) Radial pulse analysis for circadian cycle evaluation. TENCON 2013 - 2013 IEEE Region 10 Conference (31194) , p.1-4, Oct. 2013. doi :10.1109/TENCON.2013.6719 034

- Joshi P, Daruwala R, (2013) Objective evaluation of radial pulse signal. India Conference (INDICON), 2013 Annual IEEE, pp.1-4, 13-15 Dec. 2013. doi:10.1109/INDCON.2013.6726081

- Joshi P, Daruwala R, (2014) Exploring Multiscale Entropy Measures for Radial Pulse Analysis with Traditional Indian Medicine (TIM) perspective for dominant humor identification. Update Ayurveda 2014, 11-13 Dec. 2014, TNMC & BYL Nair Ch. Hospital, Mumbai, INDIA.
  Above paper was adjudged as the best paper in prize paper presentation category and awarded the 1st prize.

- Joshi P, Daruwala R, (2015) Investigation of Radial Pulse Analysis with Traditional Indian Medicine(TIM) perspective for diagnosis. HealthTech Innovations 2015 - Proceedings, pp. 51-52, 9-10 Jan. 2015, SAMEER, IIT – Bombay, INDIA.
  Above concept note was awarded 'Best Concept Note', prize in the theme: Diagnosis and treatment of diseased by Traditional Methods.

- Joshi P, Ambekar K, Daruwala R (2015) Traditional Indian Medicine (TIM) based Humor Identification - A recurrence based Approach. International Journal of Computer Applications 113(14):33-37, March 2015. doi: 10.5120/19898-1996

- Joshi PS and Daruwala RD, "Exploring radial pulse analysis using multi-scale entropy measures for pathologic condition indication," *2016 IEEE Annual India Conference (INDICON)*, Bangalore, 2016, pp. 1-5.
  doi: 10.1109/INDICON.2016.7838970